

Please **CANCEL** claims 2-11, 17-21 and 28-33 without prejudice or disclaimer.

Please **AMEND** claims 12-15, 23 and 25 as follows:

<sup>2</sup>/~~12~~. (Once Amended) A computing apparatus as recited in claim <sup>1</sup>/~~15~~, wherein said clock control unit operates to decrease the clock frequency when the temperature of said processing unit is too high.

<sup>3</sup>/~~13~~. (Once Amended) A computing apparatus as recited in claim <sup>1</sup>/~~15~~, wherein said computing apparatus is a microprocessor.

<sup>4</sup>/~~14~~. (Once Amended) A computing apparatus as recited in claim <sup>1</sup>/~~15~~, wherein said computing apparatus is a computer system.

<sup>1</sup>/~~15~~. (Once Amended) A computing apparatus, comprising:

a processing unit, said processing unit executes instructions in accordance with a clock signal having a clock frequency;

a temperature sensor that monitors temperature of said processing unit;

an activity detector that monitors activity of said processing unit; and

a clock control unit operatively connected to said processing unit and said temperature sensor and said activity detector, said clock control unit operates to alter the clock frequency of the clock signal in a gradual and dynamic manner based on the temperature of said processing unit as monitored by said temperature sensor and on the activity of said processing unit as monitored by said activity detector.

B2 8 23. (Once Amended) A computer as recited in <sup>6</sup>22, wherein the reduced power mode includes at least a sleep state.

B3 7 25. (Once Amended) A computer as recited in claim <sup>6</sup>22, wherein said fan controller uses pulse width modulation to control the speed of said fan.

Please **ADD** new claims 34 - 74.

B4 24 34. (New) A method for providing thermal management for a computer, the computer including at least a processor and a cooling fan, said method comprising:

monitoring temperature of the processor;

activating a cooling fan when the temperature of the processor indicates that primary thermal management is required; and

subsequently reducing operational clock frequency of the processor when the temperature of the processor indicates that supplemental thermal management is required even after the cooling fan has been activated.

31 35. (New) A method as recited in claim <sup>24</sup>34, wherein the cooling fan is a variable-speed fan, and

wherein said activating of the cooling fan causes the cooling fan to operate at a speed that is dependent on the temperature of the processor.

25 36. (New) A method as recited in claim <sup>24</sup>34, wherein the speed of the cooling fan is controlled using pulse-width modulation.

32 37. (New) A method as recited in claim <sup>31</sup>35, wherein when the cooling fan is initially activated, the speed of the cooling fan is relatively slow and the speed

of the cooling fan thereafter increases in a gradual manner when the temperature of the processor increases.

<sup>26</sup><sub>38</sub>. (New) A method as recited in claim <sup>24</sup><sub>34</sub>, wherein when the cooling fan is initially activated, the speed of the cooling fan is increased in a gradual manner as additional primary thermal management is needed.

<sup>33</sup><sub>39</sub>. (New) A method as recited in claim <sup>24</sup><sub>34</sub>, wherein the cooling fan is a variable-speed fan, and

wherein the primary thermal management operates the cooling fan at successively greater speeds to provide a plurality of different levels of the primary thermal management.

<sup>34</sup><sub>40</sub>. (New) A method as recited in claim <sup>33</sup><sub>39</sub>, wherein the level of the primary thermal management being performed is dependent on the temperature of the processor.

<sup>27</sup><sub>41</sub>. (New) A method as recited in claim <sup>24</sup><sub>34</sub>, wherein said activating of the cooling fan increases the speed of the cooling fan in a gradual manner to provide different levels of the primary thermal management.

<sup>28</sup><sub>42</sub>. (New) A method as recited in claim <sup>24</sup><sub>34</sub>, wherein said reducing of the operational clock frequency reduces the operational clock frequency by an amount dependent on the temperature of the processor.

<sup>25</sup><sub>43</sub>. (New) A method as recited in claim <sup>24</sup><sub>34</sub>, wherein the supplemental thermal management reduces the operational clock frequency by successively greater amounts to provide a plurality of different levels of the supplemental thermal management.

<sup>34</sup>  
44. (New) A method as recited in claim <sup>35</sup>~~43~~, wherein the level of the supplemental thermal management being performed is dependent on the temperature of the processor.

<sup>24</sup>  
45. (New) A method as recited in claim <sup>34</sup>~~34~~, wherein said reducing the operational clock frequency of the processor is performed in a gradual manner to provide different levels of the supplemental thermal management.

<sup>30</sup>  
46. (New) A method as recited in claim <sup>24</sup>~~34~~, wherein the operational clock frequency of the processor is altered with hysteresis.

<sup>37</sup>  
47. (New) A method for providing thermal management for a computer, the computer including at least a processor and a cooling fan, said method comprising:

monitoring temperature of the processor;

monitoring activity of the processor;

activating a cooling fan when the temperature and the activity of the processor indicate that primary thermal management is required; and

subsequently reducing operational clock frequency of the processor when the temperature and the activity of the processor indicate that supplemental thermal management is required even after the cooling fan has been activated.

<sup>43</sup>  
48. (New) A method as recited in claim <sup>37</sup>~~47~~, wherein the cooling fan is a variable-speed fan, and

wherein said activating of the cooling fan causes the cooling fan to operate at a speed that is dependent on the temperature and the activity of the processor.

<sup>44</sup>  
~~49~~. (New) A method as recited in claim ~~48~~<sup>43</sup>, wherein when the cooling fan is initially activated, the speed of the cooling fan is relatively slow and the speed of the cooling fan thereafter increases in a gradual manner when the temperature of the processor increases.

<sup>38</sup>  
~~50~~. (New) A method as recited in claim ~~47~~<sup>37</sup>, wherein when the cooling fan is initially activated, the speed of the cooling fan is increased in a gradual manner as additional primary thermal management is needed.

<sup>45</sup>  
~~51~~. (New) A method as recited in claim ~~47~~<sup>37</sup>, wherein the cooling fan is a variable-speed fan, and

wherein the primary thermal management operates the cooling fan at successively greater speeds to provide a plurality of different levels of the primary thermal management.

<sup>46</sup>  
~~52~~. (New) A method as recited in claim ~~51~~<sup>45</sup>, wherein the level of the primary thermal management being performed is dependent on the temperature and the activity of the processor.

<sup>39</sup>  
~~53~~. (New) A method as recited in claim ~~47~~<sup>37</sup>, wherein said activating of the cooling fan increases the speed of the cooling fan in a gradual manner to provide different levels of the primary thermal management.

<sup>40</sup>  
~~54~~. (New) A method as recited in claim ~~47~~<sup>37</sup>, wherein said reducing of the operational clock frequency reduces the operational clock frequency by an amount dependent on the temperature and the activity of the processor.

<sup>47</sup>  
~~55~~. (New) A method as recited in claim <sup>37</sup>~~47~~, wherein the supplemental thermal management reduces the operational clock frequency by successively greater amounts to provide a plurality of different levels of the supplemental thermal management.

<sup>48</sup>  
~~56~~. (New) A method as recited in claim <sup>47</sup>~~55~~, wherein the level of the supplemental thermal management being performed is dependent on the temperature and the activity of the processor.

<sup>41</sup>  
~~57~~. (New) A method as recited in claim <sup>37</sup>~~47~~, wherein said reducing the operational clock frequency of the processor is performed in a gradual manner to provide different levels of the supplemental thermal management.

<sup>42</sup>  
~~58~~. (New) A method as recited in claim <sup>37</sup>~~47~~, wherein the operational clock frequency of the processor is altered with hysteresis.

<sup>49</sup>  
~~59~~. (New) A method for providing thermal management for a computer, the computer including at least a processor and a cooling fan, said method comprising:

monitoring temperature of the processor;

comparing the temperature of the processor with at least a first temperature threshold and a second predetermined temperature, the second predetermined temperature corresponding to a greater temperature than the first predetermined temperature;

activating a cooling fan when the temperature of the processor exceeds the first predetermined temperature; and

reducing operational clock frequency of the processor when the temperature of the processor exceeds the second predetermined temperature,

wherein the cooling fan provides primary thermal management and reduction in the operational clock frequency of the processor provides secondary thermal management.

<sup>51</sup>  
60. (New) A method as recited in claim <sup>49</sup>~~59~~, wherein said method further comprises:

increasing the operational clock frequency of the processor when the temperature of the processor drops substantially below the second predetermined temperature, provided that the operational clock frequency was previously reduced by said reducing.

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<sup>52</sup>  
61. (New) A method as recited in claim <sup>51</sup>~~60~~, wherein said method further comprises:

deactivating the cooling fan when the temperature of the processor is substantially less than the first predetermined temperature, provided that the cooling fan was previously activated by said activating.

<sup>50</sup>  
62. (New) A method as recited in claim <sup>49</sup>~~59~~, wherein said method further comprises:

increasing the operational clock frequency of the processor when the temperature of the processor drops substantially below the second predetermined temperature, provided that the operational clock frequency was previously reduced by said reducing, or deactivating the cooling fan when the temperature of the processor is substantially less than the first predetermined temperature, provided that the cooling fan was previously activated by said activating.

<sup>12</sup>  
~~63~~. (New) A computer, comprising:

a processor that operates in accordance with a clock, the clock having a clock frequency;

a temperature sensor that provides a temperature indication; and

a fan; and

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a thermal manager operatively connected to said microprocessor and said fan, said thermal manager being configured to receive the temperature indication from said temperature sensor, and said thermal manager activates said fan when the temperature indication indicates that primary thermal management is required, and subsequently reduces the clock frequency of the clock for said processor when the temperature indication indicates that supplemental thermal management is required even after said fan has been activated.

<sup>13</sup>  
~~64~~. (New) A computer as recited in claim <sup>12</sup>~~63~~, wherein when said thermal manager reduces the clock frequency of the clock for said processor such reduction is performed in a gradual manner.

<sup>14</sup>  
~~65~~. (New) A computer as recited in claim <sup>12</sup>~~63~~, wherein said fan is a variable-speed fan, and

wherein when said thermal manager activates said fan, the speed of said fan is dependent on the temperature indication.

<sup>16</sup>  
~~66~~. (New) A computer as recited in claim <sup>12</sup>~~63~~, wherein the temperature indication pertains to the temperature of said processor.

<sup>17</sup>  
~~67~~. (New) A computer as recited in claim <sup>16</sup>~~66~~, wherein said processor is a microprocessor.



<sup>18</sup>  
68. (New) A computer as recited in claim <sup>12</sup>63, wherein said fan is a variable-speed fan,

wherein said computer further comprises:

an activity detector operatively connected to said processor,  
said activity detector determines an activity level of said processor, and

wherein when said thermal manager activates said fan, the speed of  
said fan is controlled based on the temperature of said processor and the  
activity level.

<sup>19</sup>  
69. (New) A computer as recited in claim <sup>18</sup>68,

wherein said thermal manager is operatively connected to said activity  
detector, and

wherein when said activity detector detects that the activity level is low,  
said thermal manager causes the clock frequency of the clock to be  
substantially reduced such that said fan need not be activated.

<sup>20</sup>  
70. (New) A computer as recited in claim <sup>12</sup>63, wherein said thermal manager  
manages the temperature of said processor to advert its overheating in a  
performance manner by using said fan for primary thermal management to  
improve thermal conditions without sacrificing some performance of said  
processor by lowering the clock frequency.

<sup>21</sup>  
71. (New) A computer as recited in claim <sup>20</sup>70, wherein the lowering of the  
clocking frequency of said processor is used for supplemental thermal  
management to improve the thermal conditions when use of said fan for  
primary thermal management is unable to stabilize the thermal conditions.

<sup>22</sup>  
72. (New) A computer as recited in claim <sup>21</sup>71, wherein with primary thermal  
management a plurality of respectively greater speeds for said fan can be  
used to attempt to stabilize the thermal conditions.

<sup>23</sup>  
73. (New) A computer as recited in claim <sup>21</sup>~~71~~, wherein with supplemental thermal management a plurality of respectively lower clock frequencies for the clock can be used to attempt to stabilize the thermal conditions.

<sup>5</sup>  
74. (New) A computer as recited in claim <sup>12</sup>~~63~~, wherein said thermal manager deactivates said fan when said processor enters a reduced power mode.

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